Ge NANOCRYSTALS EMBEDDED IN ALUMINA FOR NON VOLATILE MEMORY APPLICATIONS

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Dielectric layers with embedded semiconductor nanocrystals (NCs) are widely studied, in order to overcome difficulties of non-volatile memory devices connected with technology scale-down, and to develop Si-based light emitting diodes (LEDs). In fact, a lot of research has been dedicated to study the quantum-confined electronic states in low dimension structures of group IV indirect-gap semiconductors such as Silicon (Si) and Germanium (Ge). Artificially controlled processes and the natural formation of quantum dots (QDs) of these materials would open new possibilities for the application of these materials in novel integrated optoelectronics and microelectronics devices.

Although related metal–insulator–semiconductor (MIS) structures can be prepared on any semiconductor substrate, silicon-based structures are the most important ones for the technological development. The most common structure used for both memory and LED purposes is the metal or poly-Si/SiO_x/Si structure with Si NCs embedded in the SiO_x layer [1]. However, alumina or stacked dielectrics are also used as dielectric matrix and Ge and SiGe nanocrystals are also often formed inside these matrices. Constant shrinking of the thickness of gate dielectrics to below 2-3 nm has led to a search for alternative materials, whose dielectric constant is higher than that of SiO₂, but whose other properties remain similar to SiO₂, which is the case of Al₂O₃. A dielectric constant ($\varepsilon_{Al2O3} = 9$) more than twice that of SiO₂ ($\varepsilon_{SiO2}=3.9$), a band gap of 9eV, good mechanical properties and high temperature resistance, makes Al₂O₃ a good candidate to replace SiO₂ as a gate dielectric material and an ideal material for Si processing conditions.

Several techniques are being used to fabricate Ge NCs, such as RF co-sputtering, ion implantation, evaporation–condensation, electron beam evaporation, chemical vapour deposition, and pulsed laser deposition. In this work Ge NCs embedded in alumina thin films were deposited over Silicon (111) substrates using RF magnetron co-sputtering technique. Annealing was performed in order to improve the cristallinity of the Ge phase in the films and achieve control over the NCs size [2]

Ge NCs of suitable size and good crystalline quality were obtained by different techniques, including X-ray diffraction (XRD) (Fig. 1, inset), Raman spectroscopy (Fig. 1) and high resolution transmission electron microscopy (HRTEM) (Fig. 2a). The NCs size was estimated from Raman spectra using Fauchet and Campbell model [3] and from X-ray diffraction using Debye-Scherrer equation [4]. Statistical average diameters were obtained from HRTEM pictures.

A good agreement between the three techniques for the size estimation was observed for smallest NCs. However, for larger NCs (> 20 nm) there is a discrepancy between XRD and Raman results. For this sample the LO Raman peak presents a blue shift relative Ge bulk and X-ray peaks are shifted for higher angles relative to the Ge diamond structure. A possible explanation for this shift is the compressive stress exerted on Ge NCs by the Al_2O_3 matrix [5, 6]. Further detailed studies are in course in order to clarify both confinement and stress effects.

Poster

References:

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Figures:



a)

Fig 1: Raman spectrum of one annealed Ge/Al₂O₃ film (800° C/1hour/4.6E⁻³ mbar air pressure). The LO peak is red shifted relatively to Ge bulk. Inset: XRD spectrum, clearly revealing (220), (311) and (400) reflections corresponding to peaks from Ge diamond structure. The average NC's size increased from approximately 3.4 (as-grown) up to 7.3 nm (annealed).

> **Fig.2** – **a**) A cross section highresolution TEM micrograph of the Ge/Al₂O₃ annealed film from Fig.1; The lattice fringes from Ge NC's are visible; **b**) Ge NC's size distribution calculated for this sample was 8 ± 3 nm, in agreement with the estimation made using Debey-Scherrer formula on XRD spectrum of Fig.1.

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This work has been partially supported by the European Commission through project called SEMINANO under the contract NMP4-CT-2004- 505285, by FCT-Fundação para a Ciência e a Tecnologia-Portugal, and by the "Programa Operacional Ciência e Inovação 2010-POCI 2010" co-financed by FEDER (Fundo Comunitário Europeu) throught the Project POCTI/FIS/56930/2004. SRCP thanks FCT for the financial support (grant SFRH/BD/29657/2006).

b)

NC's size (nm)